TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LVX125F,TC74LVX125FN,TC74LVX125FT

Quad Bus Buffer

The TC74LVX125F/ FN/ FT is a high-speed CMOS quad bus buffer fabricated with silicon gate CMOS technology. Designed for use in 3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation. This device is suitable for low-voltage and battery operated systems.

This device requires the 3-state control input G to be set high to place the output into the high-impedance.

An input protection circuit ensures that 0 to 5.5V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

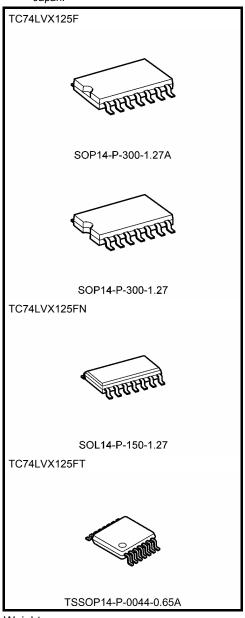
Features

- High-speed: $t_{pd} = 4.4 \text{ ns (typ.) (V}_{CC} = 3.3 \text{ V})$
- Low power dissipation: $I_{CC} = 4 \mu A \text{ (max) (Ta} = 25 ^{\circ}\text{C)}$
- Input voltage level: $V_{IL} = 0.8 \text{ V (max)} (V_{CC} = 3 \text{ V})$

$$V_{IH} = 2.0 \text{ V (min)} (V_{CC} = 3 \text{ V})$$

- · Power-down protection is provided on all inputs
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Low noise: VOLP = 0.5 V (max)
- Pin and function compatible with 74HC125

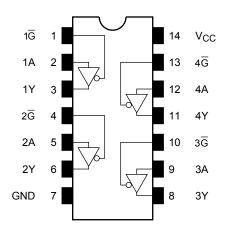
Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

SOP14-P-300-1.27A : 0.18 g (typ.) SOP14-P-300-1.27 : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.)

Pin Assignment (top view)



IEC Logic Symbol

1 G -	(1) (2)	EN	\triangleright	∇	(3)	1Y
1A - 2G -	(4)		•		(6)	2Y
2A -	(10)				(8)	3Y
3A - 4G -	(13)				(11)	4Y
4/1						

Truth Table

Inp	Outputs				
G	Α	Y			
Н	Х	Z			
L	L	L			
L	Н	Н			

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V_{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

2



Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 3.6	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100	ns/V

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics		Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit					
				Vo		Min	Тур.	Max	Min	Max					
			_		2.0	1.5	_	_	1.5	_					
	H-level	V_{IH}			3.0	2.0	_	_	2.0	_					
Input voltage					3.6	2.4	_	_	2.4	_	V				
input voitage			_		2.0	_	_	0.5	_	0.5	v				
	L-level	V _{IL}			3.0	_	_	0.8	_	8.0					
					3.6	_	_	0.8	_	0.8					
	H-level	Voн	V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -50 \mu A$	2.0	1.9	2.0	_	1.9	_	- V				
				I _{OH} = -50 μA	3.0	2.9	3.0	_	2.9	_					
Output voltage				I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_					
Output voltage	L-level	V _{OL}	V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 50 \mu A$	2.0	_	0	0.1	_	0.1					
				I _{OL} = 50 μA	3.0	_	0	0.1	_	0.1					
						ı.L	"-	"-			I _{OL} = 4 mA	3.0		_	0.36
3-state output		loz	$V_{IN} = V_{IH}$	or V _{IL}	3.6			±0.25		±2.5	μА				
Off-state current		loz	V _{OUT} = V _{CC} or GND		3.0			±0.25		12.5	μΑ				
Input leakage current		I _{IN}	V _{IN} = 5.5 \	v or GND	3.6	_	_	±0.1	_	±1.0	μΑ				
Quiescent supply	Quiescent supply current		$V_{IN} = V_{CC}$	or GND	3.6		_	4.0	_	40.0	μΑ				

3



AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition	rest Condition		Га = 25°0		Ta = -40 to 85°C		Unit	
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	
	tarri		2.7	15	_	5.8	10.1	1.0	13.5	ns
Propagation delay time	t _{pLH}			50		8.3	13.6	1.0	17.0	
1 Topagation delay time	t		3.3 ± 0.3	15		4.4	6.2	1.0	8.5	113
	t _{pHL}		3.3 ± 0.3	50		6.9	9.7	1.0	12.0	
	t _{pZL}	$R_L = 1 \text{ k}\Omega$	2.7	15	_	5.3	9.3	1.0	12.5	ns ns
Output anable time				50	_	7.8	12.8	1.0	16.0	
Output enable time	t _{pZH}		3.3 ± 0.3	15	_	4.0	5.6	1.0	7.5	
				50	_	6.5	9.1	1.0	11.0	
Output diaable time	t_{pLZ}	$R_L = 1 k\Omega$	2.7	50	_	10.0	15.7	1.0	19.0	ns
Output disable time	t_{pHZ}		3.3 ± 0.3	50	_	8.3	11.2	1.0	13.0	115
Output to output skew	t _{osLH}	(Note 1)	2.7	50	_	_	1.5	_	1.5	
Output to output skew	t _{osHL}	(Note 1)	3.3 ± 0.3	50	_	_	1.5	_	1.5	ns
Input capacitance	C _{IN}			(Note 2)	_	4	10	_	10	pF
Output capacitance	C _{OUT}		_		_	6	_	_	_	pF
Power dissipation capacitance	C _{PD}			(Note 3)	_	14	_	_	_	pF

Note 1: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$

Note 2: Parameter guaranteed by design.

Note 3: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

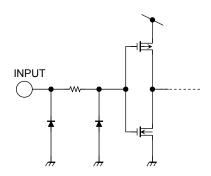
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 (per bit)$

Noise Characteristics (Ta = 25°C, input: $t_r = t_f = 3$ ns, $C_L = 50$ pF)

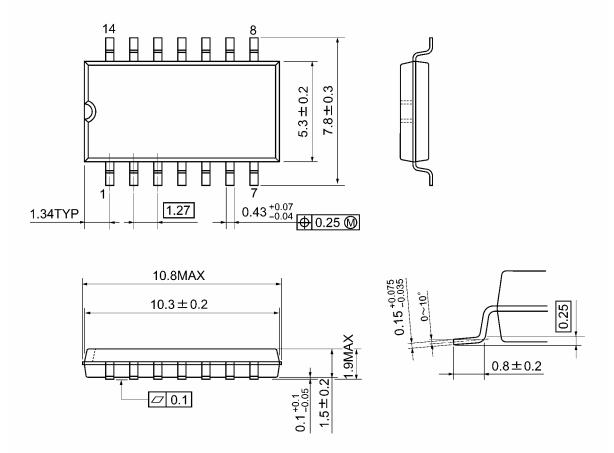
Characteristics		Symbol	Test Condition	V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic	V_{OL}	V _{OLP}	_	3.3	0.3	0.5	٧
Quiet output minimum dynamic	V_{OL}	V _{OLV}	_	3.3	-0.3	-0.5	٧
Minimum high level dynamic input voltage	V _{IH}	V _{IHD}		3.3		2.0	٧
Maximum low level dynamic input voltage	V _{IL}	V _{ILD}	——————————————————————————————————————	3.3		0.8	V

Input Equivalent Circuit



Package Dimensions

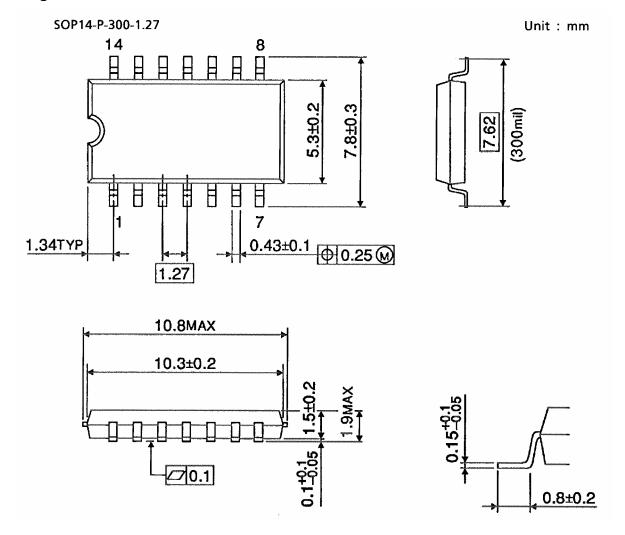
SOP14-P-300-1.27A Unit: mm



5

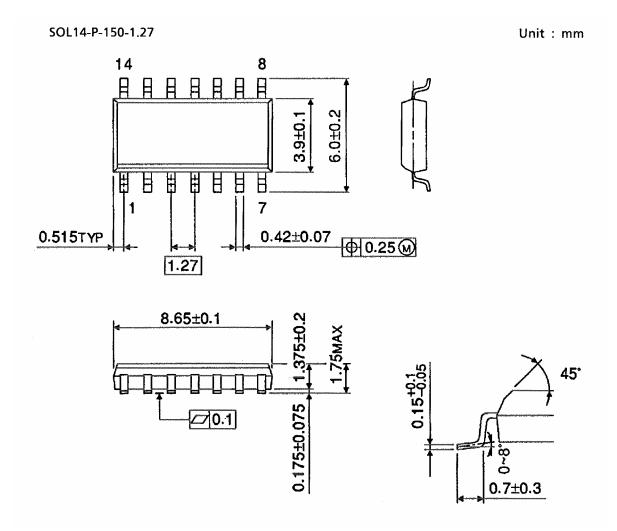
Weight: 0.18 g (typ.)

Package Dimensions



Weight: 0.18 g (typ.)

Package Dimensions (Note)

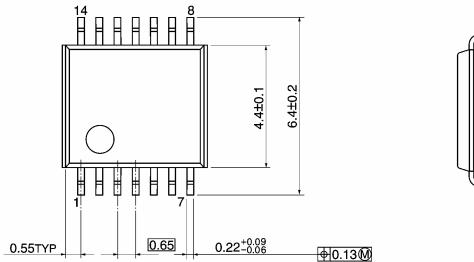


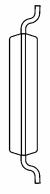
Note: This package is not available in Japan.

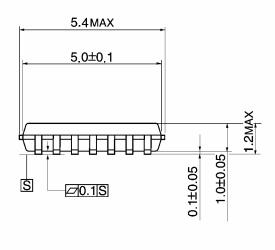
Weight: 0.12 g (typ.)

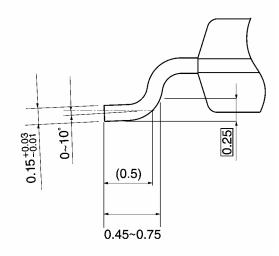
Package Dimensions

TSSOP14-P-0044-0.65A Unit: mm









Weight: 0.06 g (typ.)

Note: Lead (Pb)-Free Packages

SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A

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9